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1	BRS	L18	0	(initiator same packet).clm. and (integrated adj circuit).clm. and (packet same request).clm. and (packet not network).clm. and (arbitor or arbitration).clm. and (interconnect).clm.	US-PGPUB	2005/12/29 15:10
2	BRS	L17	1	(initiator same packet).clm. and (integrated adj circuit).clm. and (packet same request).clm. and (packet not network).clm. and (arbitor or arbitration).clm.	US-PGPUB	2005/12/29 15:10
3	BRS	L16	0	(initiator same packet) and (integrated adj circuit) and (packet same request) and (packet not network) and (arbitor or arbitration) and (circuit same descrip\$)	US-PGPUB	2005/12/29 15:08

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initiator packet request delay "circuit design" -

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[PS] A Conference Control Model for Light-weight Sessions

W Choi, K Chon - cs.columbia.edu

... on enabling technologies such as **packet** transport for audio and ... video switch, a **request** to set up an ATM virtual ... dialogue to be shown to the **initiator** when the ...

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A hypercube algorithm for GVT computation and its application in optimistic parallel simulation

SK Das, F Sarkar - Annual Simulation Symposium, 1995 - doi.ieeecs.org

... evaluation of various design options for many real world applications such as battle field simulation, VLSI logic **circuit design**, telecommunication networks ...

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[PS] Run-Time Support for Object-Oriented Distributed Programming

S Maffeis, PDLH Richter, PDKP Birman - cs.rmit.edu.au

... 26 2.2 Architecture of a CORBA Object **Request** Broker : : : : 28 ... Das Electra Toolkit folgt dem Common Object **Request** Bro- ker Architecture (CORBA) Standard. ...

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Retrograde cerebral perfusion: clinical experience in emergency and elective aortic operations

D Pagano, JA Carey, RL Patel, SM Allen, GMK Tsang, ... - Ann Thorac Surg, 1995 - ats.ctsnetjournals.org

We recently have used retrograde cerebral perfusion via the superior vena cava in association with hypothermic circulatory arrest as an adjunct to ...

Cited by 14 - [Web Search](#) - [ncbi.nlm.nih.gov](#)

Orthotopic cardiac transplantation for the failing Fontan circulation

JA Carey, JR Hamilton, CJ Hilton, JH Dark, J Forty ... - Eur J Cardiothorac Surg, 1998 - ejcts.ctsnetjournals.org

Objective: Modified Fontan procedures are now employed in several conditions unsuitable for bi-ventricular repair. Selection criteria have been relaxed. ...

Cited by 8 - [Web Search](#) - [ncbi.nlm.nih.gov](#) - [ncbi.nlm.nih.gov](#)

A comparative evaluation of accelerated solvent extraction and Polytron extraction for ...

W Zhuang, B McKague, D Reeve, J Carey - Chemosphere, 2004 - ncbi.nlm.nih.gov

Optimal conditions for extraction methods using an accelerated solvent extractor (ASE) and a Polytron homogenizer were established for quantification of ...

Cited by 3 - [Web Search](#) - [ncbi.nlm.nih.gov](#) - [csa.com](#)

Angiography contrast-induced transient cortical blindness

R Alsarraf, J Carey, BS Sires, E Pinczower - Am J Otolaryngol, 1999 - ncbi.nlm.nih.gov

Am J Otolaryngol. 1999 Mar-Apr;20(2):130-2 ...

Cited by 2 - [Web Search](#)

Book Judicial Reform in France Before the Revolution of 1789

JA Carey - 1981 - Cambridge, Mass.: Harvard University Press

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7th Annual NewEra Cardiac Care: Innovation & Technology

SVRSVRE FURTHER, DM REGURGITATION - Cardiovasc Surg, 1988 - cardenjennings.metapress.com

7th Annual NewEra Cardiac Care: Innovation & Technology ... SURGICAL VENTRICULAR RESTORATION (SVR) EVOLVES FURTHER WITH THE DEVELOPMENT OF AN ...

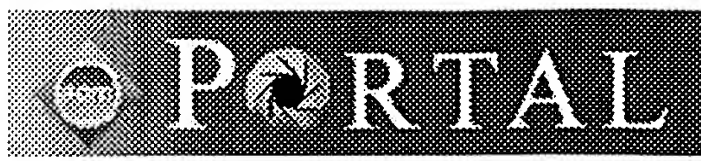
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- Enter your search terms in lower case with a space between the terms.

sales offices

You can also enter a full question or concept in plain language.

Where are the sales offices?

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John Colter, Netscape Navigator

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"museum of natural history" "museum of modern art"

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IEEE JNL IEEE Journal or Magazine

IEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

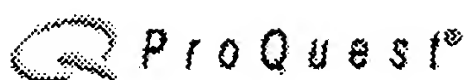
IEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

Select Article Information

- ☐ 1. **A Method for Obtaining Analog Circuits of Impedance Convertors**
Holt, A.; Carey, J.;
Circuits and Systems, IEEE Transactions on [legacy, pre - 1988]
Volume 15, Issue 4, Dec 1968 Page(s):420 - 425
[AbstractPlus](#) | Full Text: [PDF](#)(456 KB) IEEE JNL
- ☐ 2. **Comment on 'A high-quality double integrator building-block for active fil**
Reddy, et al.
Carey, J.;
Circuits and Systems, IEEE Transactions on
Volume 30, Issue 4, Apr 1983 Page(s):251 - 252
[AbstractPlus](#) | Full Text: [PDF](#)(248 KB) IEEE JNL
- ☐ 3. **Consumer adoption of new communication technologies**
Carey, J.;
Communications Magazine, IEEE
Volume 27, Issue 8, Aug. 1989 Page(s):28 - 32
Digital Object Identifier 10.1109/MCOM.1989.34693
[AbstractPlus](#) | Full Text: [PDF](#)(604 KB) IEEE JNL
- ☐ 4. **Femtosecond laser-assisted microstructuring of silicon for novel detecto**
display technologies
Carey, J.E.; Mazur, E.;
Lasers and Electro-Optics Society, 2003. LEOS 2003. The 16th Annual Meetin
Volume 2, 2003 Page(s):481 - 482 vol.2
Digital Object Identifier 10.1109/LEOS.2003.1252883
[AbstractPlus](#) | Full Text: [PDF](#)(274 KB) IEEE CNF
- ☐ 5. **A fast algorithm for the restoration of blurred binary images**
Carey, J.-A.; de Jager, G.;
Communications and Signal Processing, 1989. COMSIG 1989. Proceedings., :
Conference on
23 June 1989 Page(s):91 - 92
Digital Object Identifier 10.1109/COMSIG.1989.129023
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- ☐ 1. **Early changes in left ventricular function after aortic valve replacement for isolated aortic stenosis**
Jin, Xu Y, Pepper, John R, Brecker, Stephen J, Carey, John A, Gibson, Derek G. The American Journal of Cardiology. New York: Dec 1, 1994. Vol. 74, Iss. 11; p. 1142

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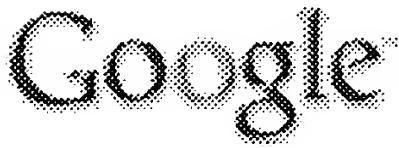
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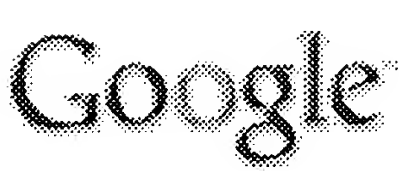
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NCR53C710PG; NCR Corporation, **Microelectronics** Products Division, ... Avoid touching areas of **integrated** circuitry; static discharge. can damage **circuits**. ...

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[PDF] [MVME167 Single Board Computer Installation Guide](#)

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... always disconnect power and discharge **circuits** before touching ... 345 East 47th St. ... I, order number NCR53C710DM NCR Corporation **Microelectronics** Products Division ...

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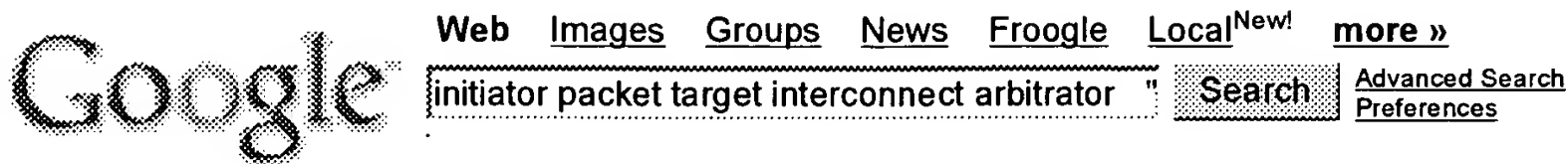
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there are also numerous proprietary buses and **interconnect** methods for ...

to better The su The Application specific **integrated circuit** (ASIC) containing a ...

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[John Fallone Thu Sep 21 00:20:49 EDT 2000 Hamming Code: this is ...](#)

... PCI bus is synchronous, all transactions are between the **initiator** and the **target**

(master and ... to get a copy of the answer sheet forthe **packet** we received ...

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[Low power, high speed communications bus - US Patent 6141765](#)

A high speed **interconnect** system which permits The high speed operation of ...

due to The high speed of The bus, conventional buses in which A **target** may ...

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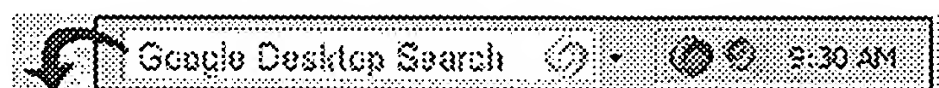
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monolithic **integrated circuit** that implements HDLC processing, and PCI Bus ...

PCI bus to the intended **target** or **initiator** in the core device. ...

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2	BRS	L2	82	(initiator same packet) and (integrated adj circuit) and target and interconnect	USPAT; EPO; JPO; DERWEN T; IBM_TDB	2005/12/29 13:49
3	BRS	L3	21	(initiator same packet) and (integrated adj circuit) and target and interconnect and (target same circuit)	USPAT; EPO; JPO; DERWEN T; IBM_TDB	2005/12/29 13:49
4	BRS	L4	7	(initiator same packet) and (integrated adj circuit) and target and interconnect and (target same circuit) and (packet not network)	USPAT; EPO; JPO; DERWEN T; IBM_TDB	2005/12/29 14:13
5	BRS	L5	7	(initiator same packet) and (integrated adj circuit) and target and interconnect and (target same circuit) and (packet not network) and description	USPAT; EPO; JPO; DERWEN T; IBM_TDB	2005/12/29 14:14
6	BRS	L6	17	(initiator same packet) and (integrated adj circuit) and (packet same request) and (packet not network)	USPAT; EPO; JPO; DERWEN T; IBM_TDB	2005/12/29 14:15
7	BRS	L7	0	(initiator same packet) and (integrated adj circuit) and (packet same request) and (packet not network) and (arbitor same arbitration)	USPAT; EPO; JPO; DERWEN T; IBM_TDB	2005/12/29 14:15
8	BRS	L8	12	(initiator same packet) and (integrated adj circuit) and (packet same request) and (packet not network) and (arbitor or arbitration)	USPAT; EPO; JPO; DERWEN T; IBM_TDB	2005/12/29 14:16

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9	BRS	L9	2	(initiator same packet) and (integrated adj circuit) and (packet same request) and (packet not network) and (arbitor or arbitration) and (circuit same descrip\$)	USPAT; EPO; JPO; DERWEN T; IBM_TDB	2005/12/29 14:21
10	BRS	L10	481	STMicroelectronics-Ltd.as.	USPAT; EPO; JPO; DERWEN T; IBM_TDB	2005/12/29 14:21
11	BRS	L11	55	STMicroelectronics-Ltd.as. and packets	USPAT; EPO; JPO; DERWEN T; IBM_TDB	2005/12/29 14:22
12	BRS	L12	4	STMicroelectronics-Ltd.as. and packets and IC	USPAT; EPO; JPO; DERWEN T; IBM_TDB	2005/12/29 14:42
13	BRS	L13	2	"6460174".pn.	USPAT; EPO; JPO; DERWEN T; IBM_TDB	2005/12/29 14:49
14	BRS	L14	713	716/18.ccls.	USPAT; EPO; JPO; DERWEN T; IBM_TDB	2005/12/29 15:07
15	BRS	L15	638	703/14.ccls.	USPAT; EPO; JPO; DERWEN T; IBM_TDB	2005/12/29 15:07